# Simulation of 7- Level AC–AC Sparse Modular Multilevel Converter

# M. Anka Rao, R. Mayuri

Abstract- 7-level AC-AC Sparse modular multilevel converter (SMMC) is proposed in this paper. It is a bi-directional multilevel converter and it is employed in high power and high voltage applications. It contains full bridge and half bridge SM's on the middle arm, and low frequency converters on each side of the converter. In this converter more number of IGBT switches can work under soft switching mode. Control scheme is also proposed in this paper, it is utilized to obtain the capacitor voltage balancing. Capacitor voltages have some oscillations, harmonics will be decreased by applying modified PWM technique. Number of levels of the converter increases, voltage harmonics will be decreased and also switching frequency of the full bridge and half bridge arm IGBTs is decrease. The appropriateness of the 7-level AC/AC SMMC with Control scheme will be estimated by utilizing the MATLAB/SIMULINK software.

*Index Terms*—7-level AC/AC SMMC, Control design, Capacitor voltage balancing, Modified PWM technique.

#### I. INTRODUCTION

Now a days, multilevel converters have broad range of applications in many industrial applications such as FACTS devices, HVDC lines and renewable energy resource interfaces [1] – [3]. Multilevel converters can be categorised into three types. Those are diode clamped converter(DCC), Flying capacitor converter(FCC) and Cascaded H- bridge converter(HBC). If number of levels of the converter increases, there should be a complexity in capacitor voltage adjusting in DCC, FCC requires more number of capacitor and cascaded H- bridge converter requires more number of individual dc sources [5] - [6]. To overcome such limitations of the converters, modular multilevel converter (MMC) is proposed in 2003. MMC contains low voltage switches and offering the low harmonic distortions. Now a days, MMC [7] - [9] based HVDC systems are used for transmitting the power up to GW range. MMC is also have some drawbacks, i.e more number of IGBT switches can operate under hard switching mode and circulating current is also more. To overcome such limitations of MMC, a new bi-directional converter, i.e sparse modular multilevel Converter(SMMC) is proposed [10].

SMMC contains fewer number of components than compared with the other types of converter topologies. In this multilevel converter more number of IGBT switches can work under soft switching mode. In this proposed converter, by inserting the third harmonic frequencies the capacitor voltage gets balanced. FFT analysis for 5-level SMMC as shown in

M. Anka Rao, Assistant Professor, Dept. of EEE, JNTU College of engineering & technology, Anantapur, Andhra Pradesh, India, 7702968080 R. Mayuri, PG Scholar, Dept. of EEE, JNTU College of engineering & technology, Anantapur, Andhra Pradesh, India, 9110757420 fig.1.1. It gives the value for total harmonic distortion (THD). To reduce the THD value, levels of the converter has to be increased. To reduce the THD value, 7-level SMMC is used in place of 5-level SMMC. 7-level SMMC is explained with control scheme in the following sections.



Fig.1.1 FFT analysis for 5-level SMMC

## II. 7-LEVEL AC - AC SMMC

Fig. 2.1 shows the circuit diagram of 1 - Ø N-level AC/AC SMMC. Fig. 2.2 shows that circuit diagram of 1 - Ø 7-level AC/AC SMMC. It contains low frequency converter on each side, and middle arm contains three full bridge SM's on upper leg and three half bridge SM's on lower leg. By inserting the proper number of SM's in the lower and upper legs of the middle arm, the necessary voltage on the each side of the converter can be obtained. Fig. 2.3 shows that simplistic diagram of 3 - Ø SMMC. No circulating current between different phases of 3 - Ø SMMC, because they are separated from each other by a 3 - ∅ isolating transformer. Zero circulating current in the converter can be reduced by placing one reverse biased IGBT switch on each arm of the full bridge unfolder circuit. For higher number of levels, converter requires two or more reverse biased IGBT switches, on each arm of full bridge unfolder circuit. Both full bridge and half bridge unfolder circuits operate under zero voltage switching(ZVS) condition

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Fig. 2.1 Circuit diagram of 1 - Ø N-level SMM



Fig. 2.2 Circuit diagram of 1 - Ø 7 – level SMMC.



Fig. 2.3 Simplistic Diagram of 3 - Ø SMMC

# III. CONTROL SCHEME FOR 7 – LEVEL SMMC

Fig. 3.1 shows that block diagram of control design. The proposed 7-level AC/AC SMMC can be controlled by using abc-dq reference frame theory. In this reference theory, AC side current can be controlled by using phase locked loop(PLL) on each side of the converter. PLL is employed to attain the synchronization mechanism. Two reference generators provides the reference ac currents to the current controller.

 $P_{aref}$  in grid A - side, determines the magnitude and direction of active or true power transferred. In this proposed converter, true power flows from Grid – A side to Grid – B side. So, low frequency converter on the Grid – A side can act as a rectifier, while low frequency converter on the Grid – B side can acts as an inverter circuit. The injected true and reactive powers to the grid A and B are obtained from the equations (1) & (2).

$$P = \frac{VV_s}{X} \sin \delta \qquad ------ (1)$$
$$Q = \frac{V_s^2 - VV_s \cos \delta}{X} \qquad ------ (2)$$

Where V and  $V_s$  are the AC-side and grid voltages respectively,  $\delta$  is the angle between the AC- side and grid-side voltages and X is the filter reactance. The reactive powers  $Q_{aref}$  and  $Q_{bref}$  are regulated to arbitrary values with in the ratings of the converter.



Fig. 3.1 Block diagram for control scheme

The proposed controlled scheme contains a current controller on the each side, which is used, to provide the expected true and reactive power interchange with the grid. Fig. 3.2 shows the block diagram for current controller. Current controller contains PI controller, which is employed to enhance the steady state response.

Harmonics and oscillations in the capacitor voltage waveform can be decreased by applying modified PWM technique to converter. By changing the energy stored in the half bridge and full bridge arm capacitors, balance between the AC-side power and arm power can be achieved. On the full bridge arm side, capacitor voltage balancing can be maintained by introducing the third harmonic voltages.

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Fig. 3.2 Block diagram for current controller

# IV. SIMULATION RESULTS

Fig. 4.1 shows that Simulink diagram of three phase 7- level SMMC. In this, Grid A operated at 50 HZ frequency and Grid B operated at 60 HZ frequency. Fig. 4.2 shows that Simulink diagram of subsystem that is single phase 7 – level SMMC.



Fig. 4.1 Simulink diagram of three phase 7 - level SMMC

Fig .4.3 shows that Simulink diagram of subsystem. Fig. 4.4 shows that Simulink diagram of subsystem1. Fig. 4.5 shows that Simulink diagram of current controller.



Fig. 4.2 Simulink diagram of Single phase 7- level SMMC



Fig. 4.3 Simulink diagram of subsystem



Fig. 4.4 Simulink diagram of subsystem1



Fig. 4.5 Simulink diagram of current controller

Fig. 4.6 shows that input voltage and current waveforms on Grid A side. Fig. 4.7 shows that output voltage and current waveforms on Grid B side. Fig. 4.8 (a) & (b) shows that capacitor Voltage waveforms of half bridge unfolder circuit and full bridge unfolder circuit. Fig. 4.9 shows that true and reactive power variations of a 7 – level SMMC. Fig. 4.10 shows the FFT analysis of output voltage waveform.



*Fig. 4.6 Input voltage and current waveforms on Grid - A side.* 



Fig. 4.7(a) Output voltage waveform on Grid - B side



Fig. 4.7 (b) Output Current Waveforms on Grid - B side



Fig. 4.8 (a) Capacitor voltage Waveforms of half bridge unfolder circuit



Fig. 4.8(b) Capacitor voltage waveforms of full bridge unfolder circuit



Fig. 4.9(a) True power variation of 7 – level SMMC



Fig. 4.9(b) Reactive power variation of 7 – level SMMC



Fig. 4.10 FFT analysis of output voltage waveform

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# V. CONCLUSION

In this paper, 7 – level AC - AC SMMC is proposed and control scheme with modified PWM technique is also discussed. In this converters more number of IGBT switches can operate under soft switching mode. So, losses in the converter is less. Hence efficiency of the converter has to be increased. By applying modified PWM technique to the converter, the oscillations and harmonics of the capacitor voltage waveform is reduced. From FFT analysis of 5-level and 7-level SMMC we can conclude that, number of levels of the converter increases THD will be reduced. The Simulation results attained from the MATLAB software shows the appropriateness of the 7 – level AC-AC SMMC.

## APPENDIX

Simulation Parameters

PARAMETER	RATING
Grid – A Frequency	50 Hz
Grid – B Frequency	60 Hz
Grid – A and Grid – B	20 KV, 18.5 KV
Voltages	
Sub Module Capacitor	4 mF
Mean cell Capacitor	2000 V
Voltage	
Filter + Grid	5 mH
inductance	
Filter + Grid	10mΩ
resistance	

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